

FIG. 1

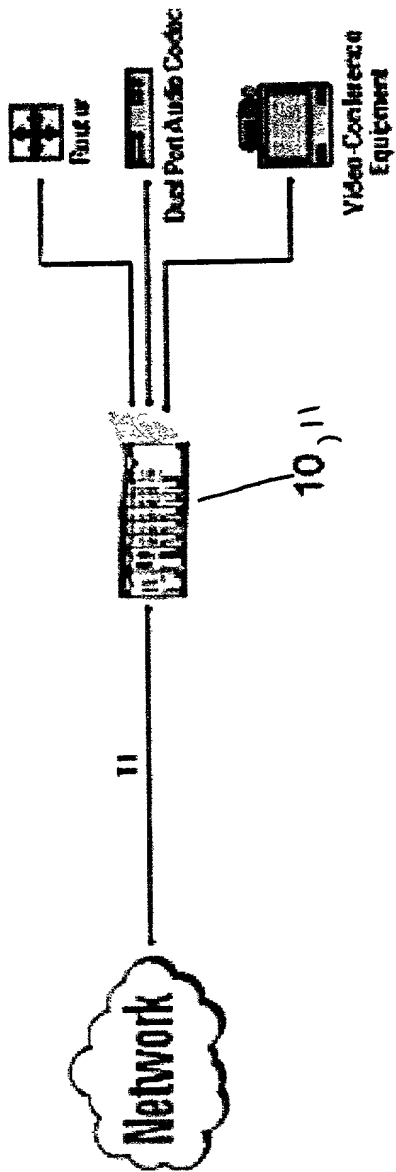


FIG. 2

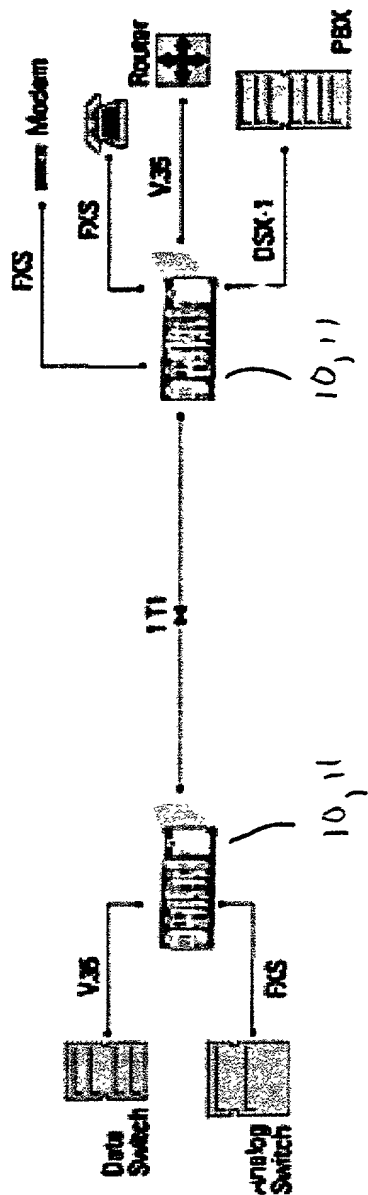


FIG. 3

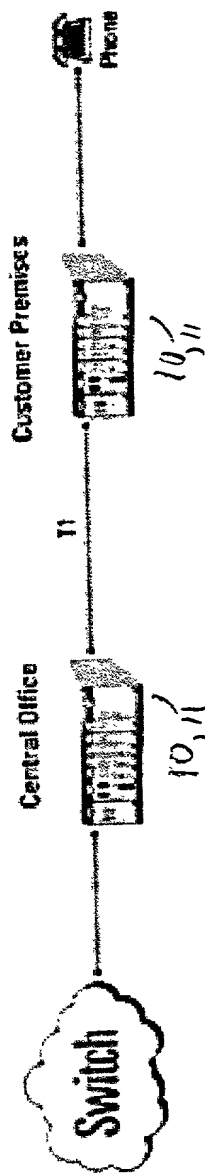


FIG. 4(a)

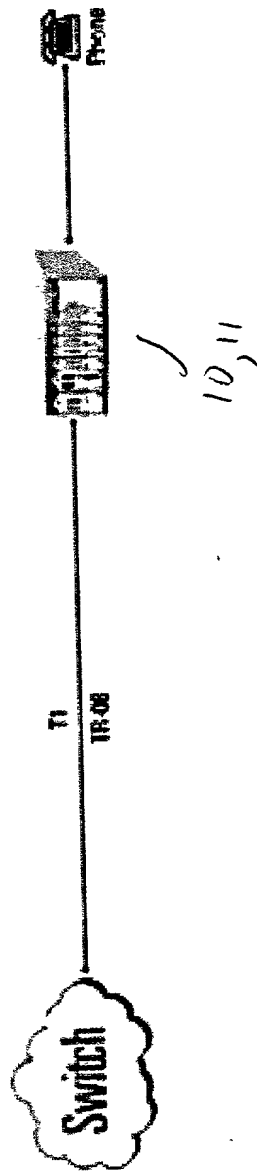


FIG. 4(b)

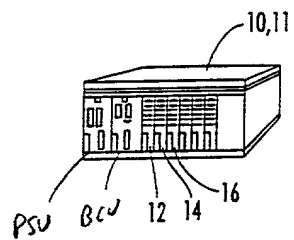


FIG. 5

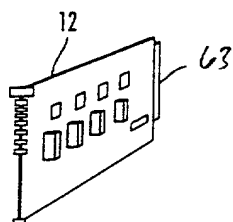


FIG. 6

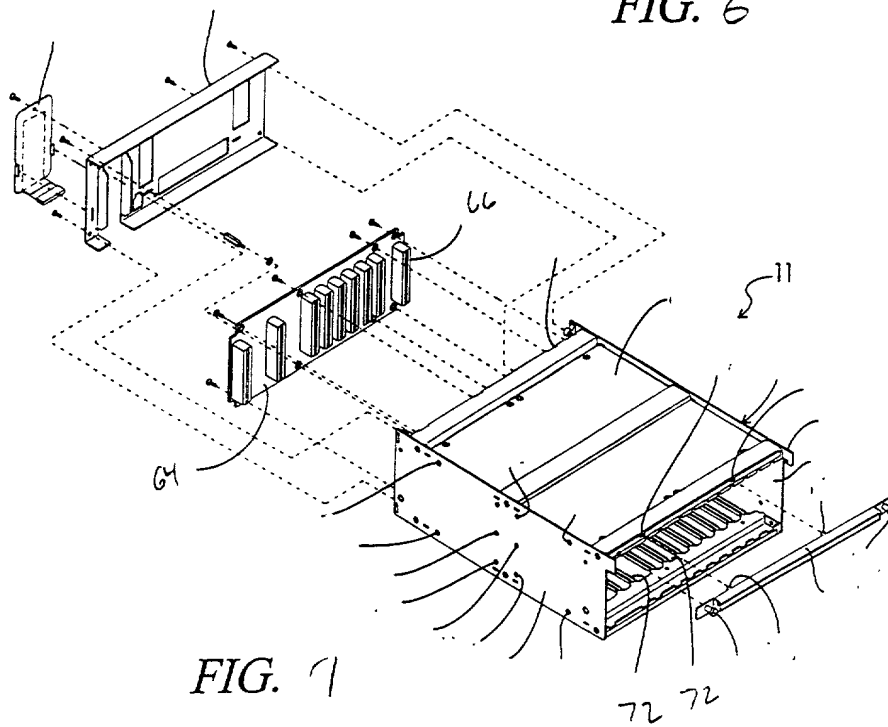


FIG. 7

09334939 094001

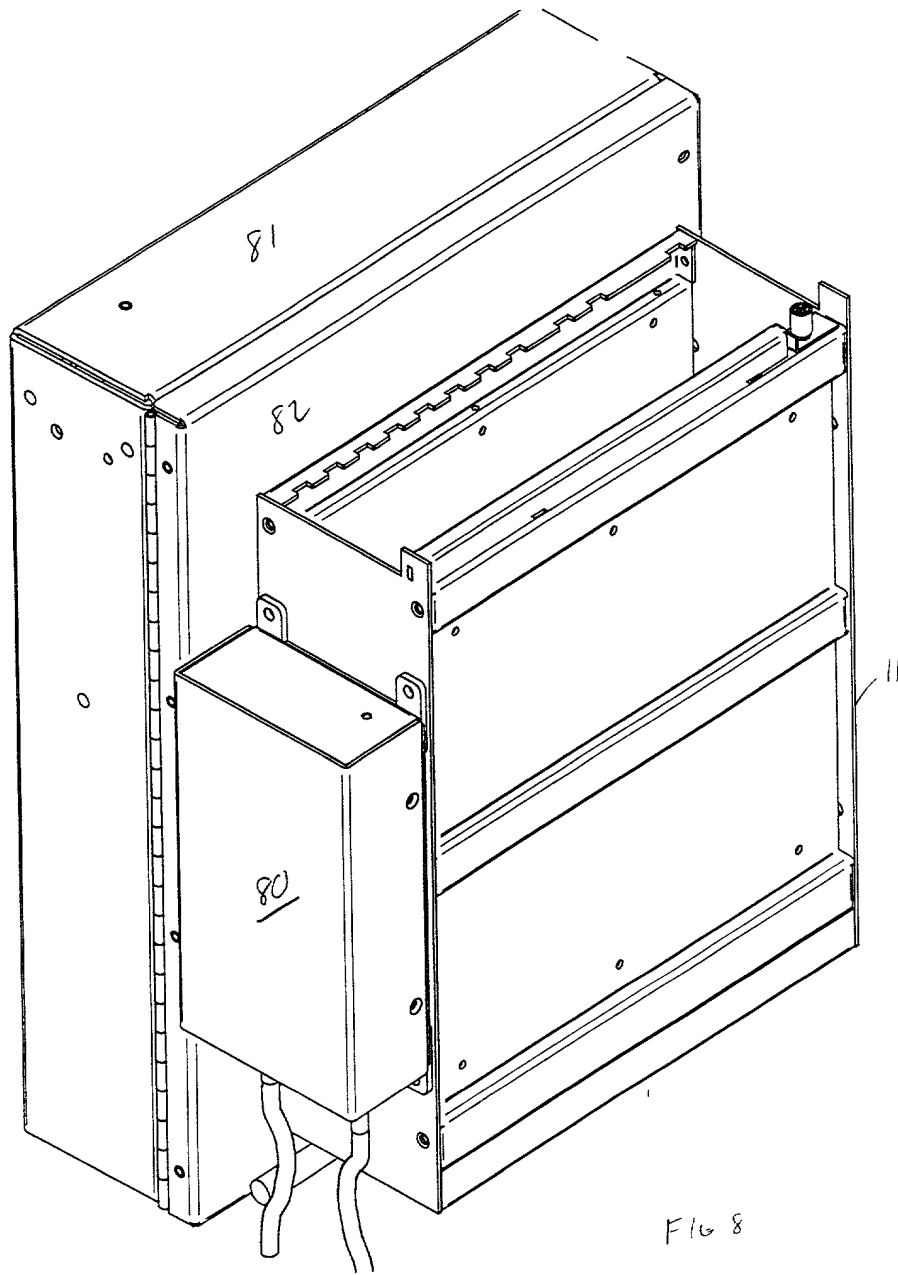


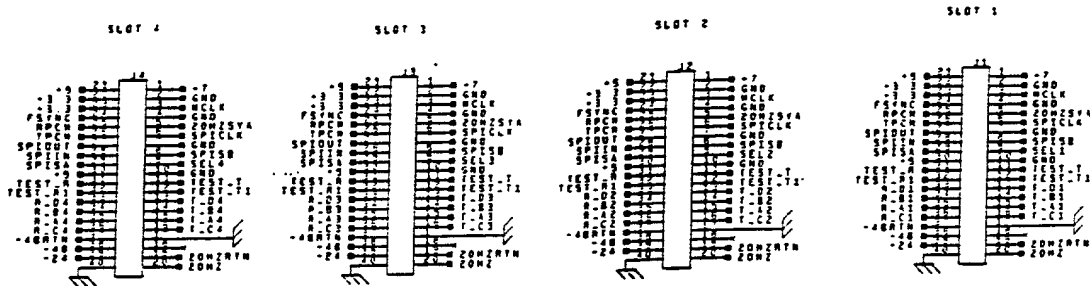
FIG 8

The image contains three hand-drawn diagrams related to the PAM slot of a PDP-11 computer:

- Top Left Diagram:** A wiring diagram for the PAM slot. It shows a connector with pins labeled 1 through 24. Pins 1-12 are labeled with various signals: 1-4 (GND, GND, GND, GND), 5-8 (GND, GND, GND, GND), 9-12 (GND, GND, GND, GND). Pins 13-24 are labeled with various signals: 13-16 (GND, GND, GND, GND), 17-20 (GND, GND, GND, GND), 21-24 (GND, GND, GND, GND). The diagram is labeled "PAM SLOT" and "32".
- Top Right Diagram:** A wiring diagram for the PAM slot. It shows a connector with pins labeled 1 through 24. Pins 1-12 are labeled with various signals: 1-4 (GND, GND, GND, GND), 5-8 (GND, GND, GND, GND), 9-12 (GND, GND, GND, GND). Pins 13-24 are labeled with various signals: 13-16 (GND, GND, GND, GND), 17-20 (GND, GND, GND, GND), 21-24 (GND, GND, GND, GND). The diagram is labeled "PAM SLOT" and "33".
- Bottom Diagram:** A schematic diagram showing the power supply for the PAM slot. It includes a 48V input, a 48V output, and a 48V output with a 47μF capacitor. The diagram is labeled "PAM SLOT" and "34".

Figure 9

09834533-034307



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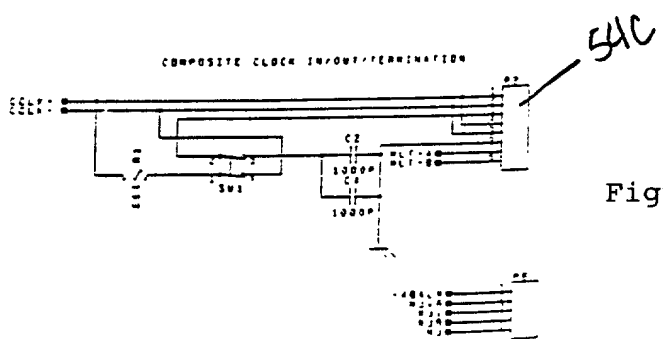
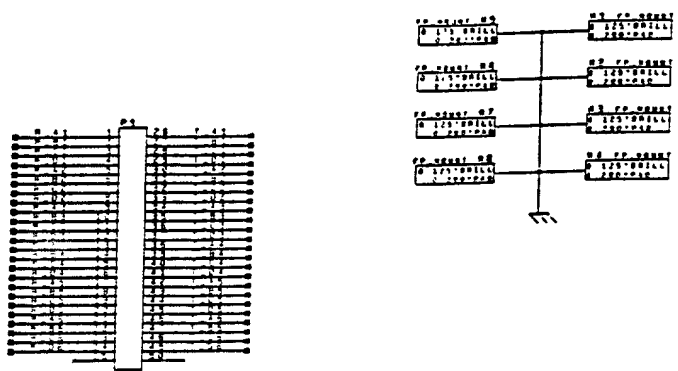


Figure 9 (Continued)

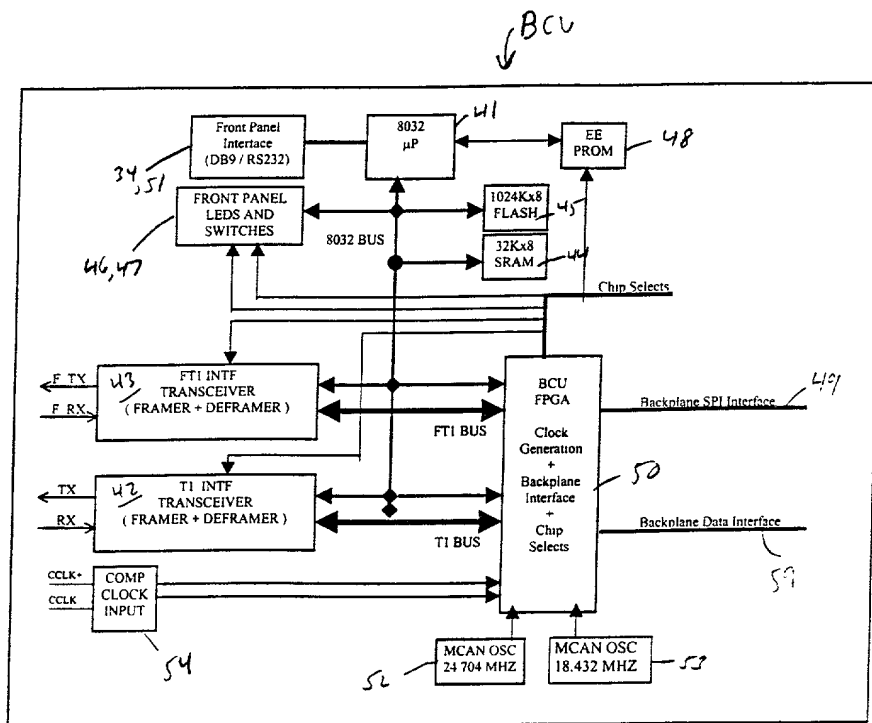


Fig. 10

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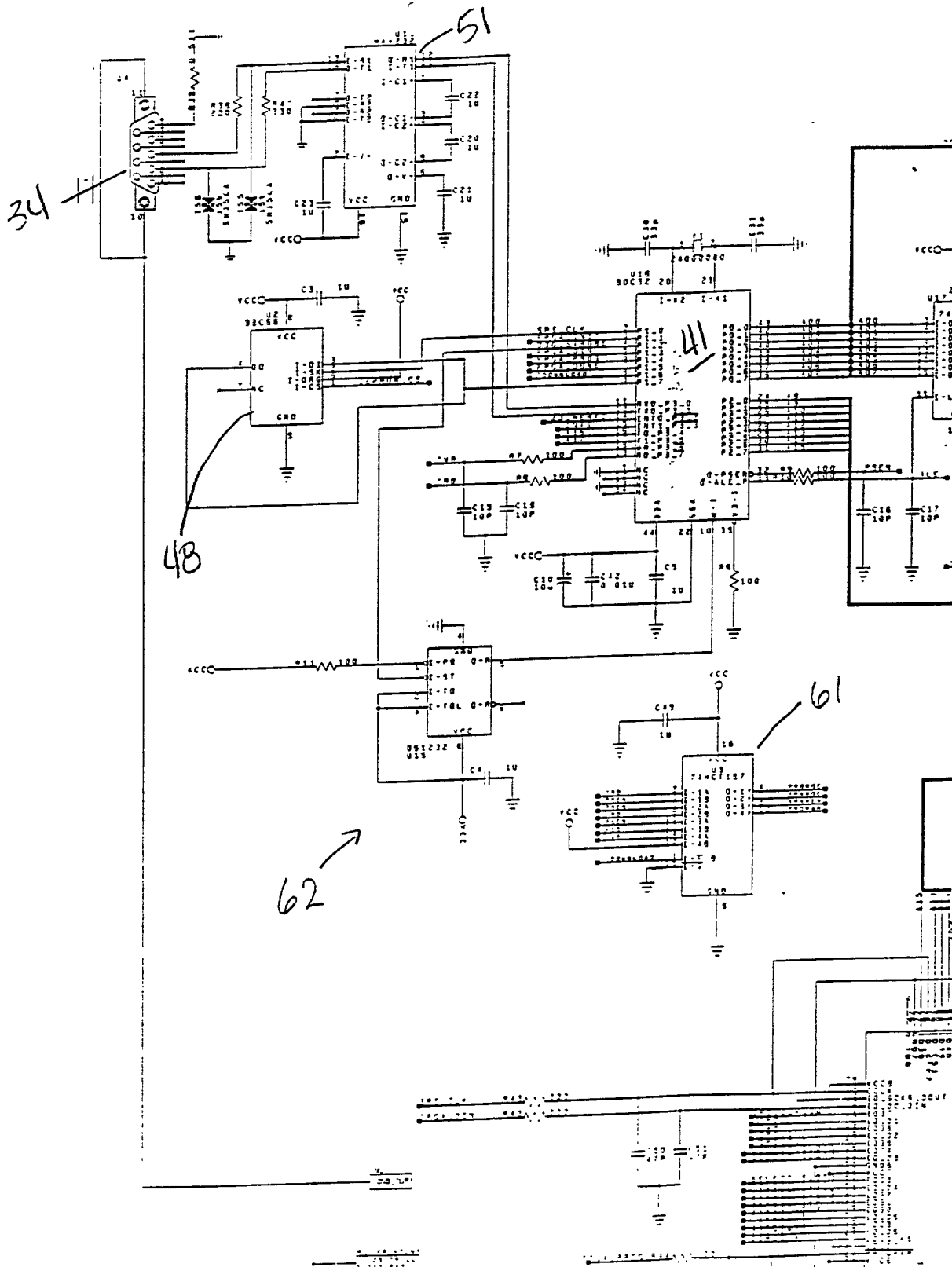
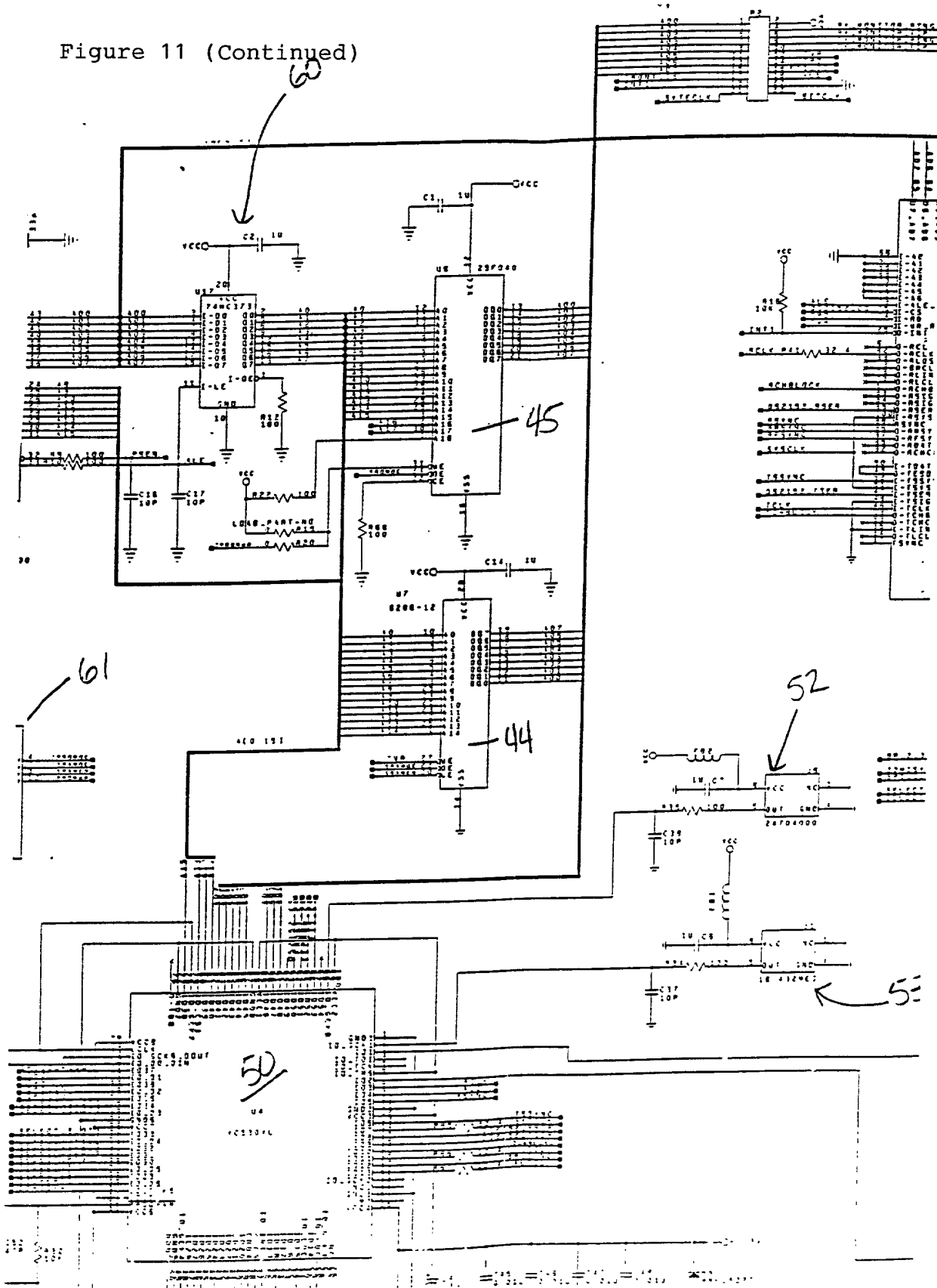


Figure 11

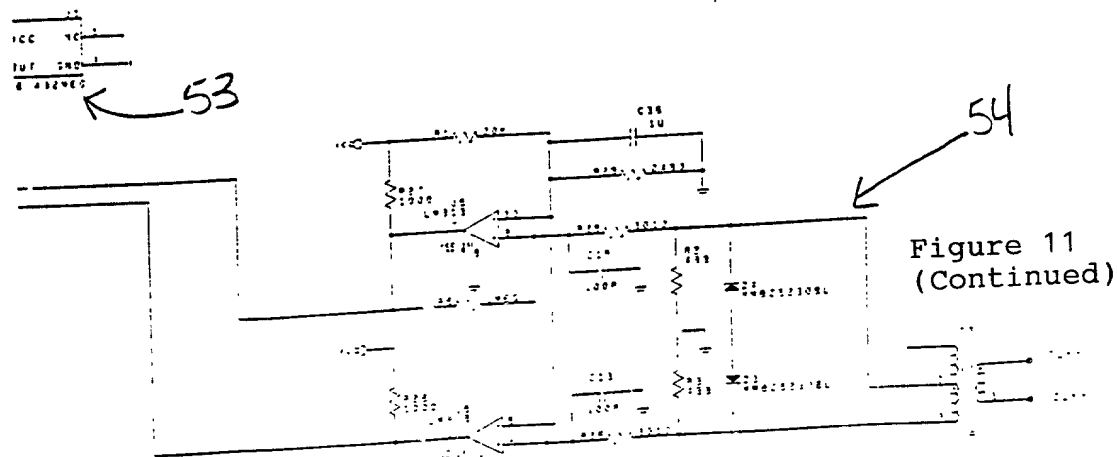
Variable	Mean	SD	Min	Max
Age	34.5	12.5	18	65
Gender	0.5	0.5	0	1
Marital status	0.6	0.5	0	1
Education	12.5	2.5	9	16
Income	1500	500	500	3000
Health status	0.8	0.2	0	1
Employment status	0.7	0.3	0	1
Home ownership	0.6	0.5	0	1
Vehicle ownership	0.4	0.5	0	1
Travel frequency	0.3	0.4	0	1
Travel distance	100	50	0	200
Travel time	30	15	0	60
Travel cost	50	20	0	100
Travel satisfaction	0.7	0.3	0	1
Travel frequency (log)	0.3	0.4	0	1
Travel distance (log)	100	50	0	200
Travel time (log)	30	15	0	60
Travel cost (log)	50	20	0	100
Travel satisfaction (log)	0.7	0.3	0	1
Travel frequency (log)	0.3	0.4	0	1
Travel distance (log)	100	50	0	200
Travel time (log)	30	15	0	60
Travel cost (log)	50	20	0	100
Travel satisfaction (log)	0.7	0.3	0	1
Travel frequency (log)	0.3	0.4	0	1
Travel distance (log)	100	50	0	200
Travel time (log)	30	15	0	60
Travel cost (log)	50	20	0	100
Travel satisfaction (log)	0.7	0.3	0	1
Travel frequency (log)	0.3	0.4	0	1
Travel distance (log)	100	50	0	200
Travel time (log)	30	15	0	60
Travel cost (log)	50	20	0	100
Travel satisfaction (log)	0.7	0.3	0	1
Travel frequency (log)	0.3	0.4	0	1
Travel distance (log)	100	50	0	200
Travel time (log)	30	15	0	60
Travel cost (log)	50	20	0	100
Travel satisfaction (log)	0.7	0.3	0	1
Travel frequency (log)	0.3	0.4	0	1
Travel distance (log)	100	50	0	200
Travel time (log)	30	15	0	60
Travel cost (log)	50	20	0	100
Travel satisfaction (log)	0.7	0.3	0	1
Travel frequency (log)	0.3	0.4	0	1
Travel distance (log)	100	50	0	200
Travel time (log)	30	15	0	60
Travel cost (log)	50	20	0	100
Travel satisfaction (log)	0.7	0.3	0	1
Travel frequency (log)	0.3	0.4	0	1
Travel distance (log)	100	50	0	200
Travel time (log)	30	15	0	60
Travel cost (log)	50	20	0	100
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Travel frequency (log)	0.3	0.4	0	1
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Travel frequency (log)	0.3	0.4	0	1
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Travel frequency (log)	0.3	0.4	0	1
Travel distance (log)	100	50	0	200
Travel time (log)	30	15	0	60
Travel cost (log)	50	20	0	100
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Travel cost (log)	50	20	0	100
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Travel frequency (log)	0.3	0.4	0	1
Travel distance (log)	100	50	0	200
Travel time (log)	30	15	0	60
Travel cost (log)	50	20	0	100
Travel satisfaction (log)	0.7	0.3	0	1
Travel frequency (log)	0.3	0.4	0	1
Travel distance (log)	100	50	0	200
Travel time (log)	30	15	0	60
Travel cost (log)	50	20	0	



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BCU Slot Connector



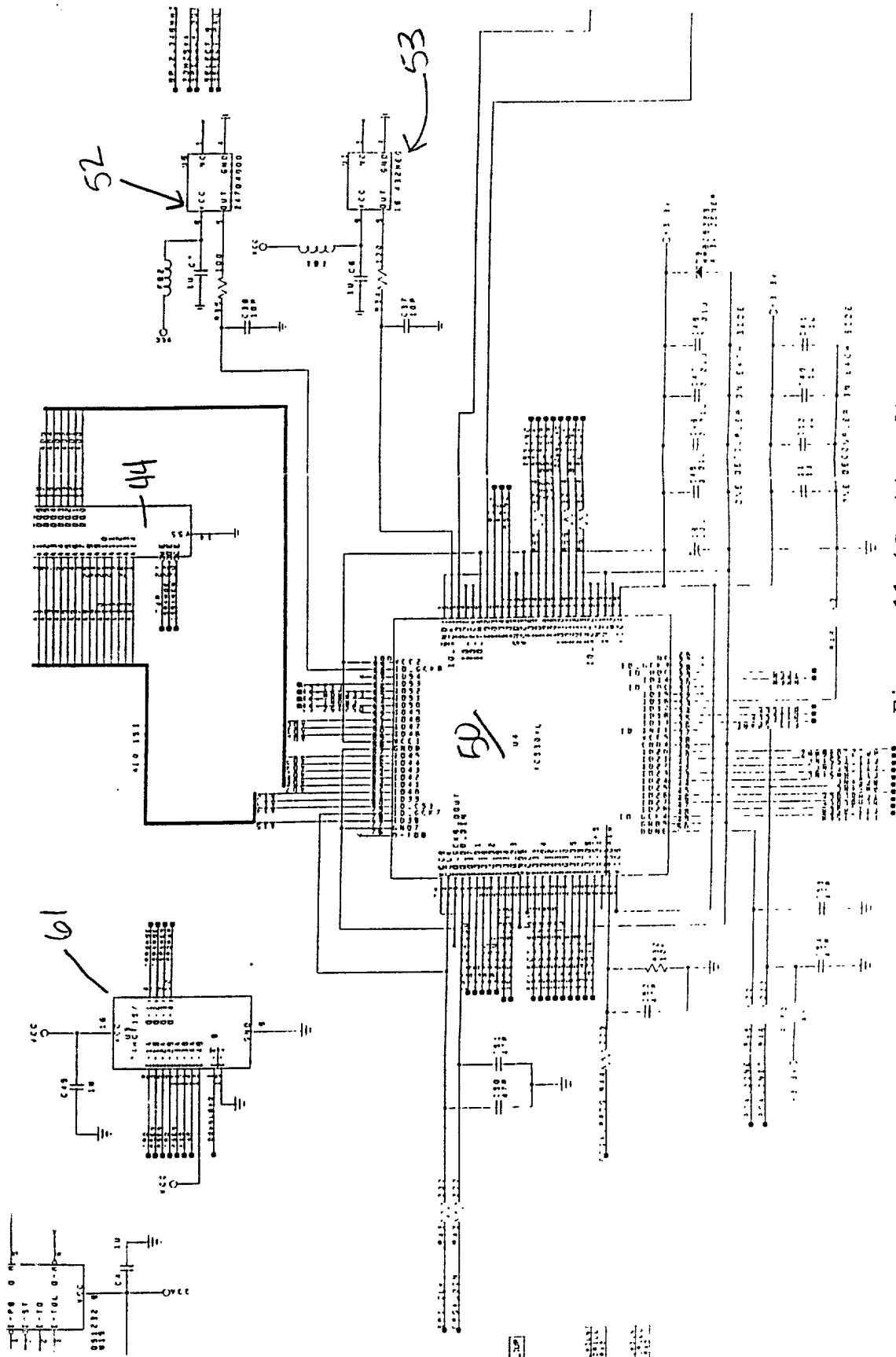


Figure 11 (Continued)

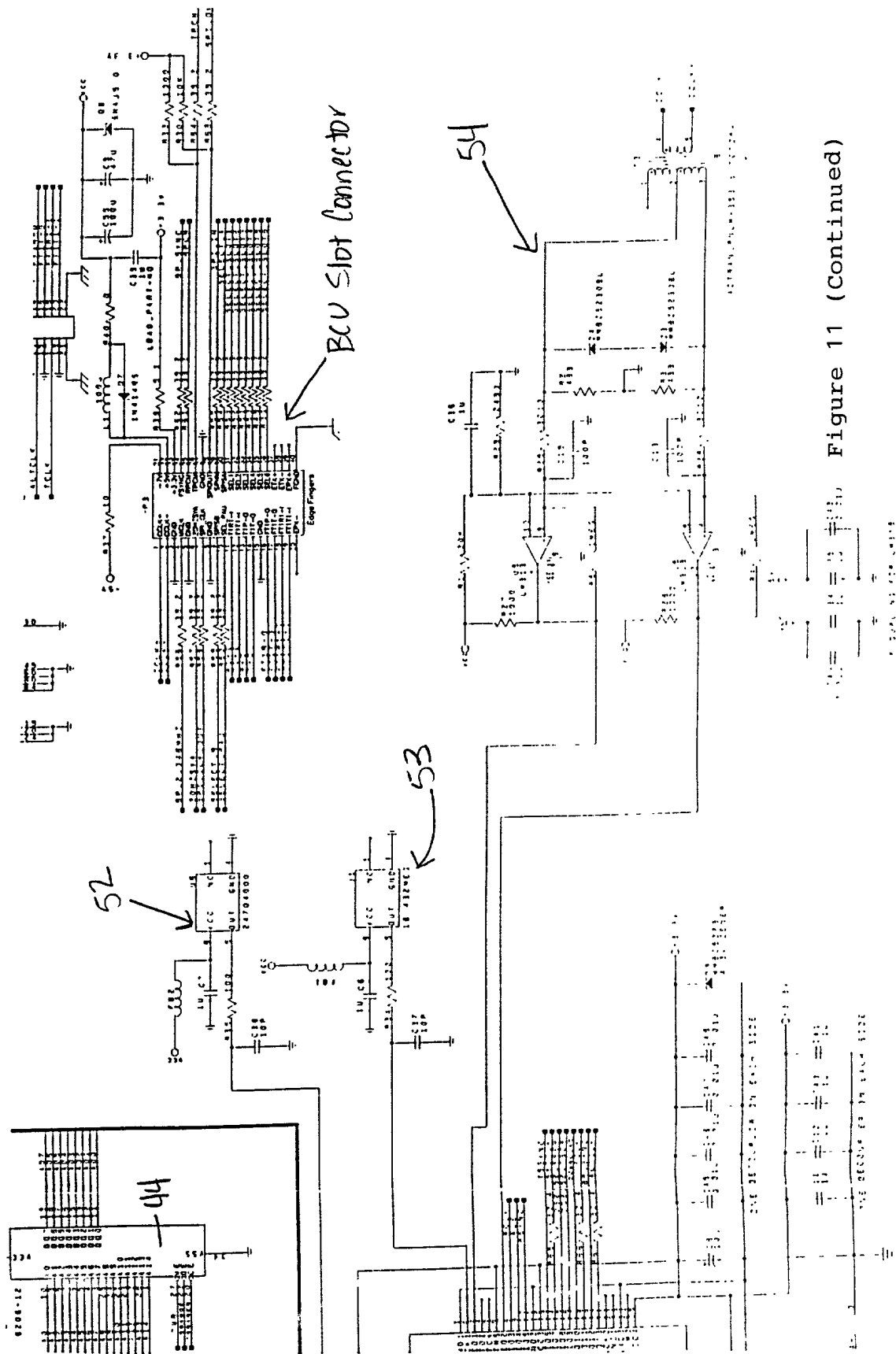


Figure 11 (Continued)

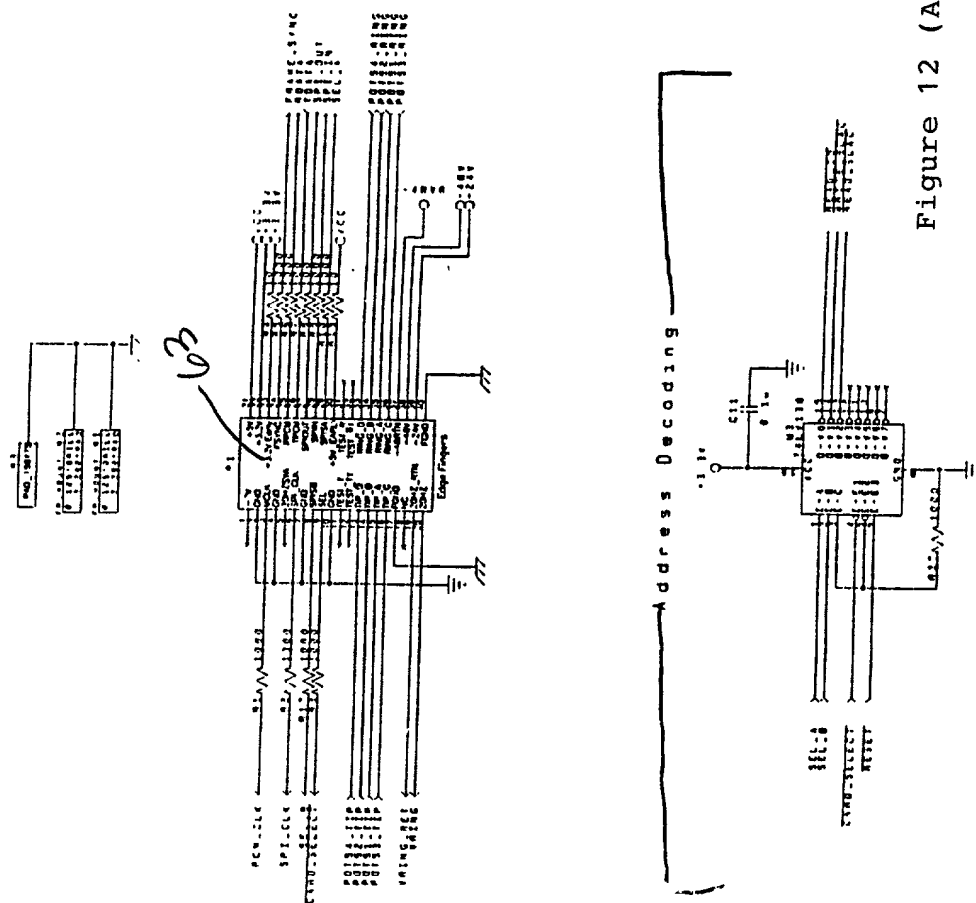
[illegible]

Figure 12 (A)

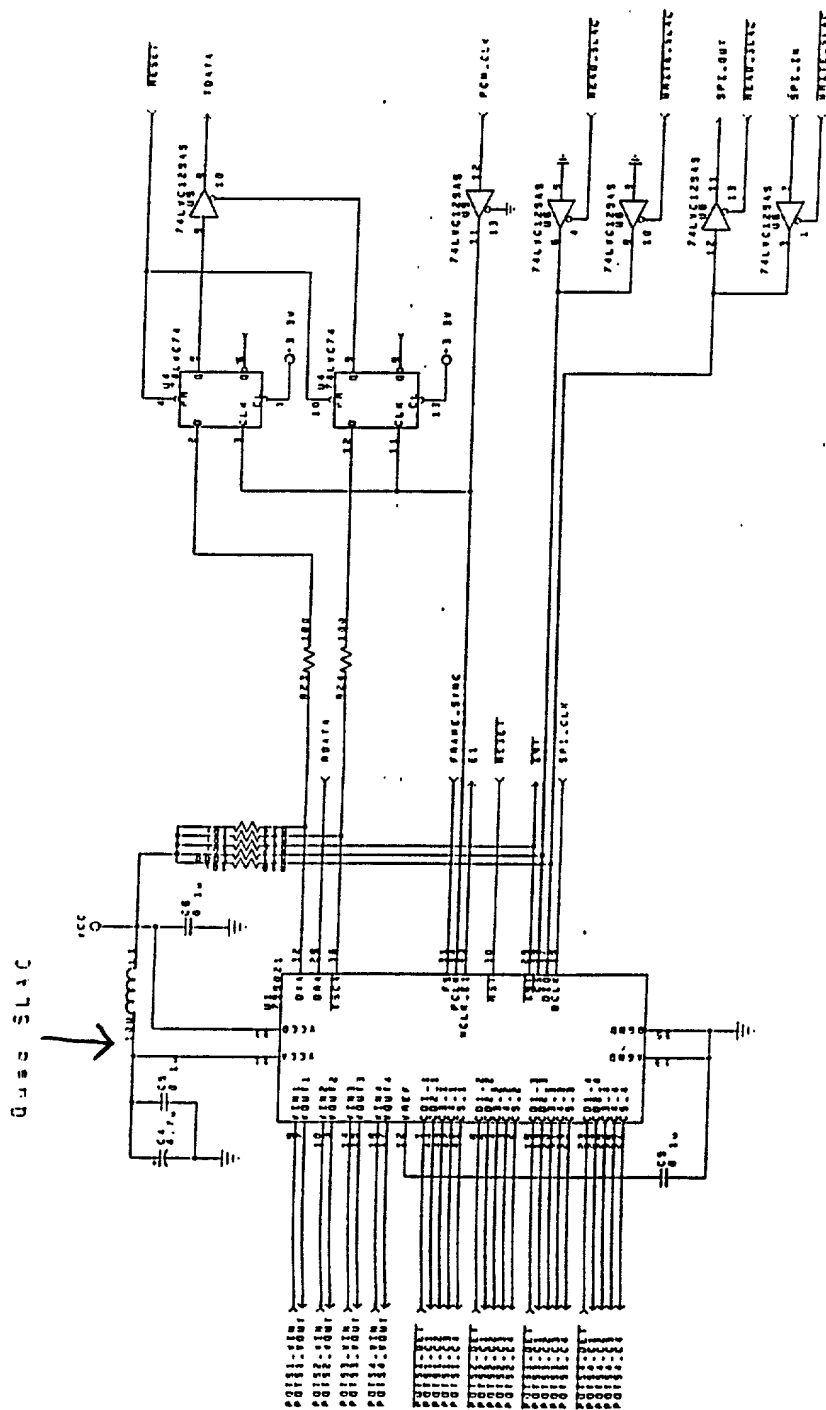


Figure 12(A) (Continued)

Interrupt, Card IO and Battery Switch Register

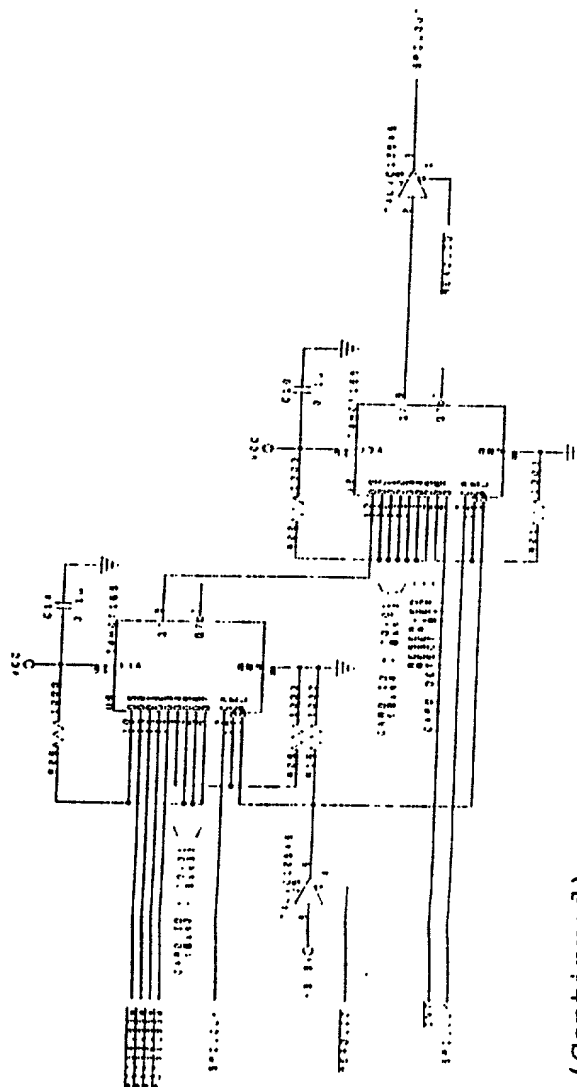


Figure 12(A) (Continued)

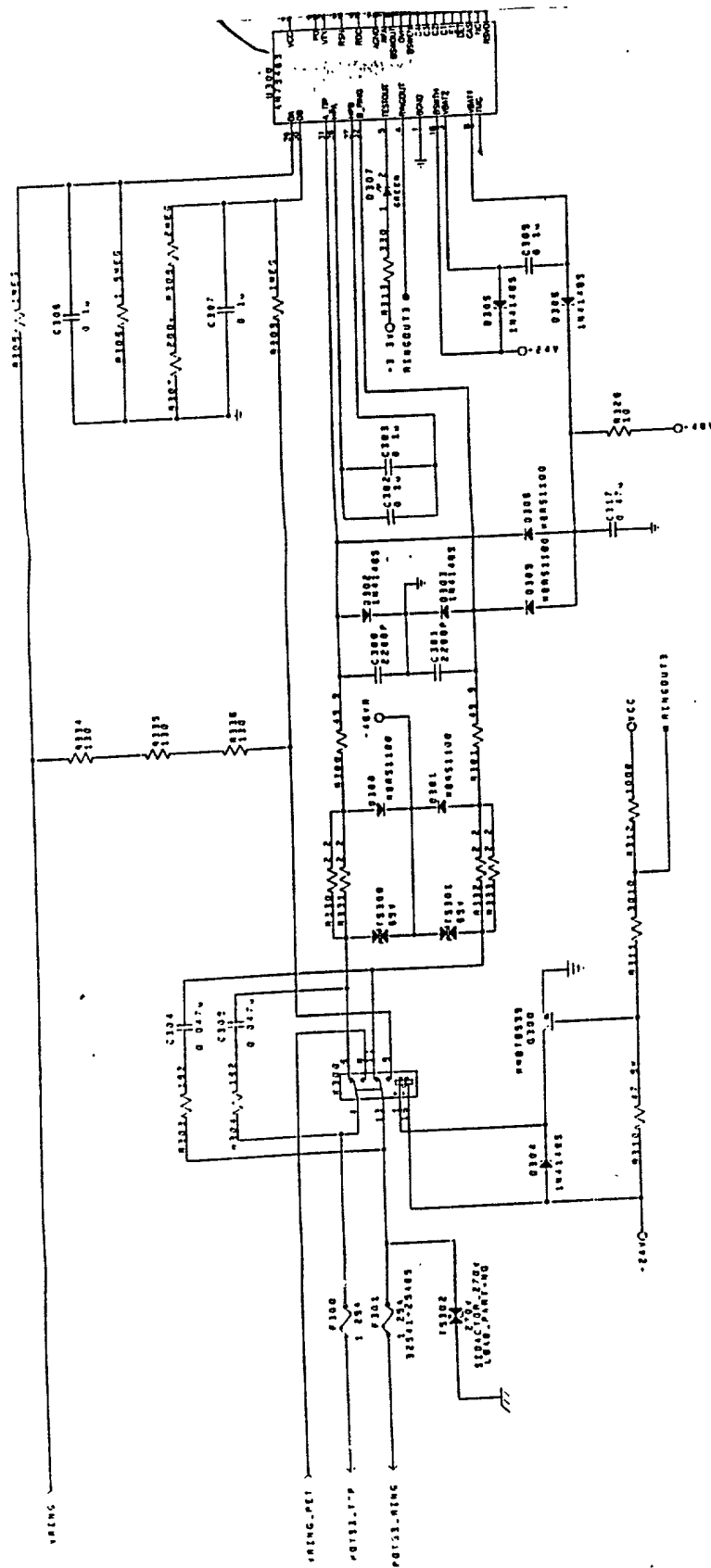
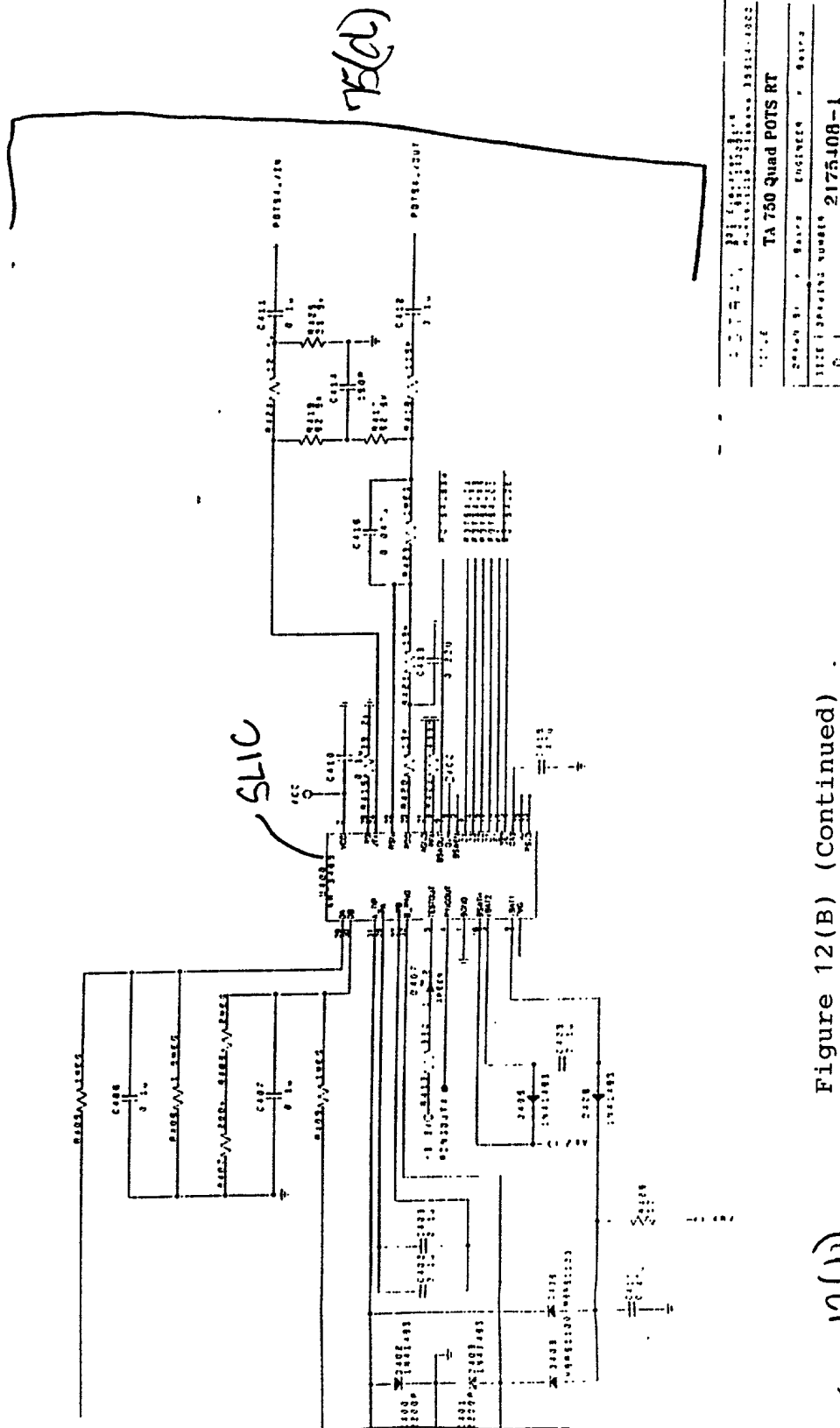


Figure 12(B) (Continued)

Variable	Mean	SD	Min	Max
Age	35.2	12.5	18	65
Gender	Male	10	0	20
Marital status	Married	15	0	30
Education	High school	5	0	10
Occupation	Unemployed	10	0	20
Income	Low	15	0	30
Health status	Good	10	0	20
Stress level	High	15	0	30
Life satisfaction	Low	10	0	20
Depression	High	15	0	30
Loneliness	High	15	0	30
Self-esteem	Low	10	0	20
Resilience	Low	10	0	20
Optimism	Low	10	0	20
Gratitude	Low	10	0	20
Forgiveness	Low	10	0	20
Empathy	Low	10	0	20
Compassion	Low	10	0	20
Kindness	Low	10	0	20
Generosity	Low	10	0	20
Patience	Low	10	0	20
Humility	Low	10	0	20
Modesty	Low	10	0	20
Shyness	Low	10	0	20
Introversion	Low	10	0	20
Neuroticism	High	15	0	30
Extraversion	Low	10	0	20
Agreeableness	Low	10	0	20
Conscientiousness	Low	10	0	20
Openness	Low	10	0	20
Stability	Low	10	0	20
Emotion regulation	Low	10	0	20
Attention	Low	10	0	20
Memory	Low	10	0	20
Reasoning	Low	10	0	20
Imagination	Low	10	0	20
Intuition	Low	10	0	20
Insight	Low	10	0	20
Wisdom	Low	10	0	20
Knowledge	Low	10	0	20
Skills	Low	10	0	20
Abilities	Low	10	0	20
Talents	Low	10	0	20
Gifts	Low	10	0	20
Strengths	Low	10	0	20
Weaknesses	High	15	0	30
Flaws	High	15	0	30
Defects	High	15	0	30
Shortcomings	High	15	0	30
Limitations	High	15	0	30
Obstacles	High	15	0	30
Barriers	High	15	0	30
Challenges	High	15	0	30
Problems	High	15	0	30
Difficulties	High	15	0	30
Struggles	High	15	0	30
Trials	High	15	0	30
Tests	High	15	0	30
Experiences	High	15	0	30
Lessons	High	15	0	30
Wisdom	High	15	0	30
Insights	High	15	0	30
Realizations	High	15	0	30
Discoveries	High	15	0	30
Findings	High	15	0	30
Conclusions	High	15	0	30
Results	High	15	0	30
Outcomes	High	15	0	30
Effects	High	15	0	30
Impacts	High	15	0	30
Consequences	High	15	0	30
Reactions	High	15	0	30
Responses	High	15	0	30
Behaviors	High	15	0	30
Actions	High	15	0	30
Choices	High	15	0	30
Decisions	High	15	0	30
Plans	High	15	0	30
Goals	High	15	0	30
Aspirations	High	15	0	30
Dreams	High	15	0	30
Hopes	High	15	0	30
Wishes	High	15	0	30
Desires	High	15	0	30
Needs	High	15	0	30
Wants	High	15	0	30
Cravings	High	15	0	30
Longings	High	15	0	30
Yearnings	High	15	0	30
Longing	High	15		



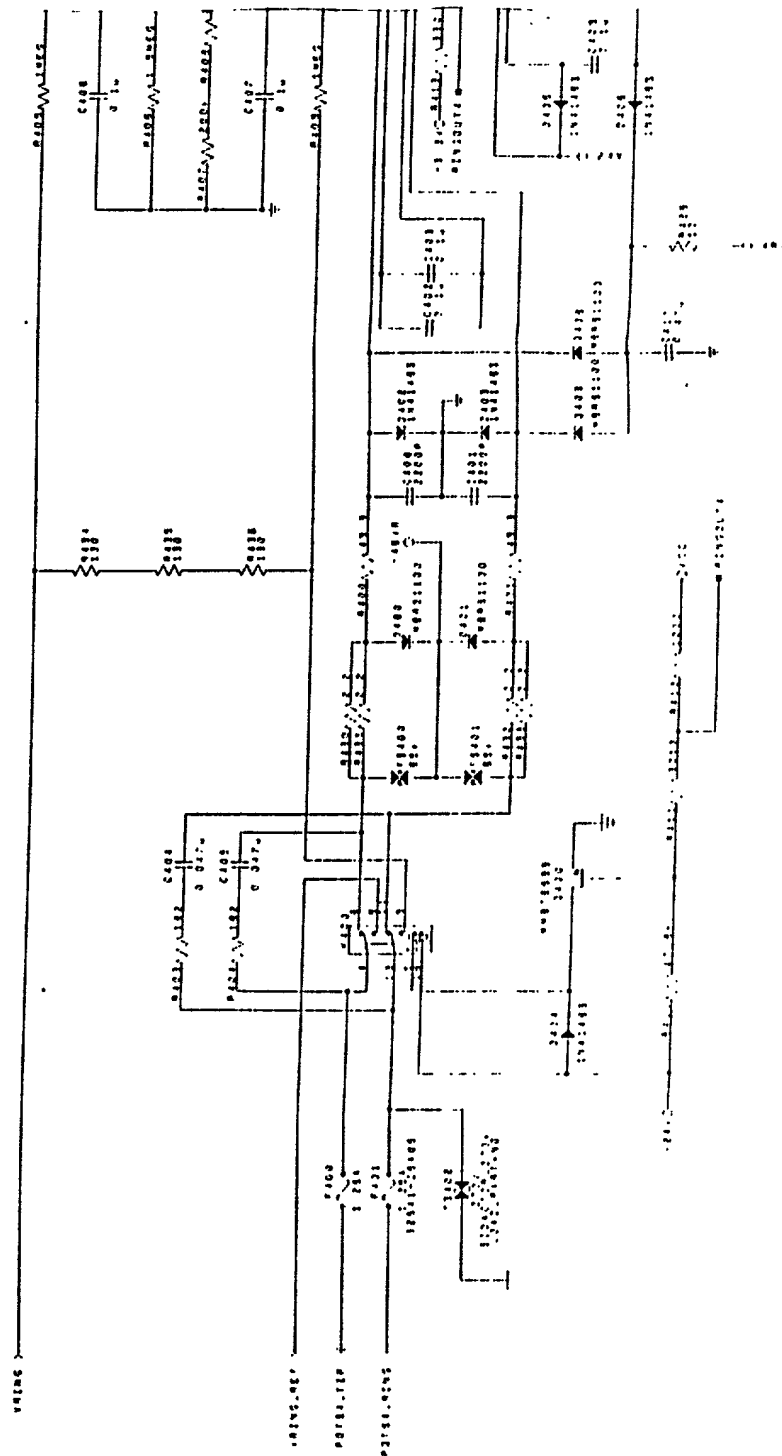


Figure 12(B) (Continued)

Fig. 12(d)

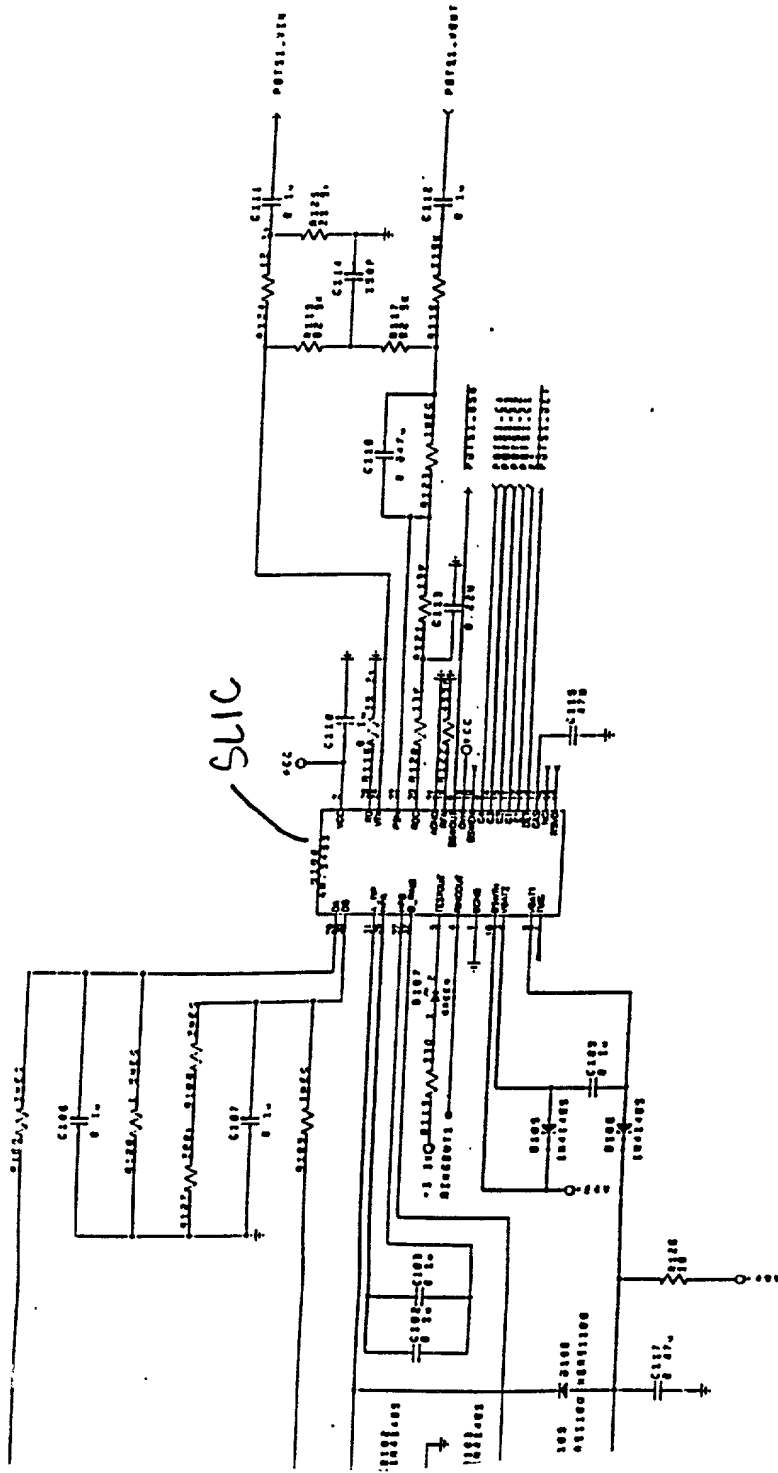


Figure 12(c)

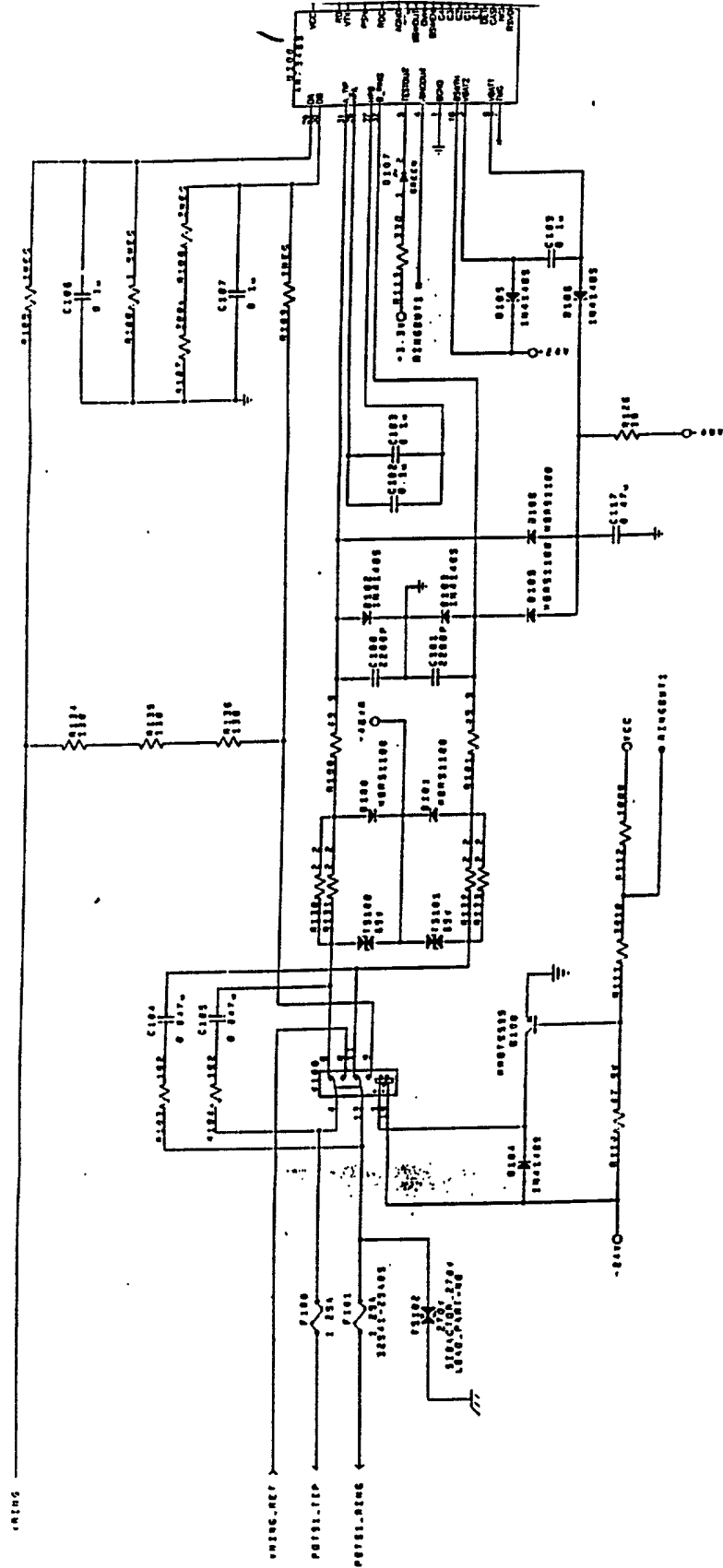


Figure 12(C) (Continued)

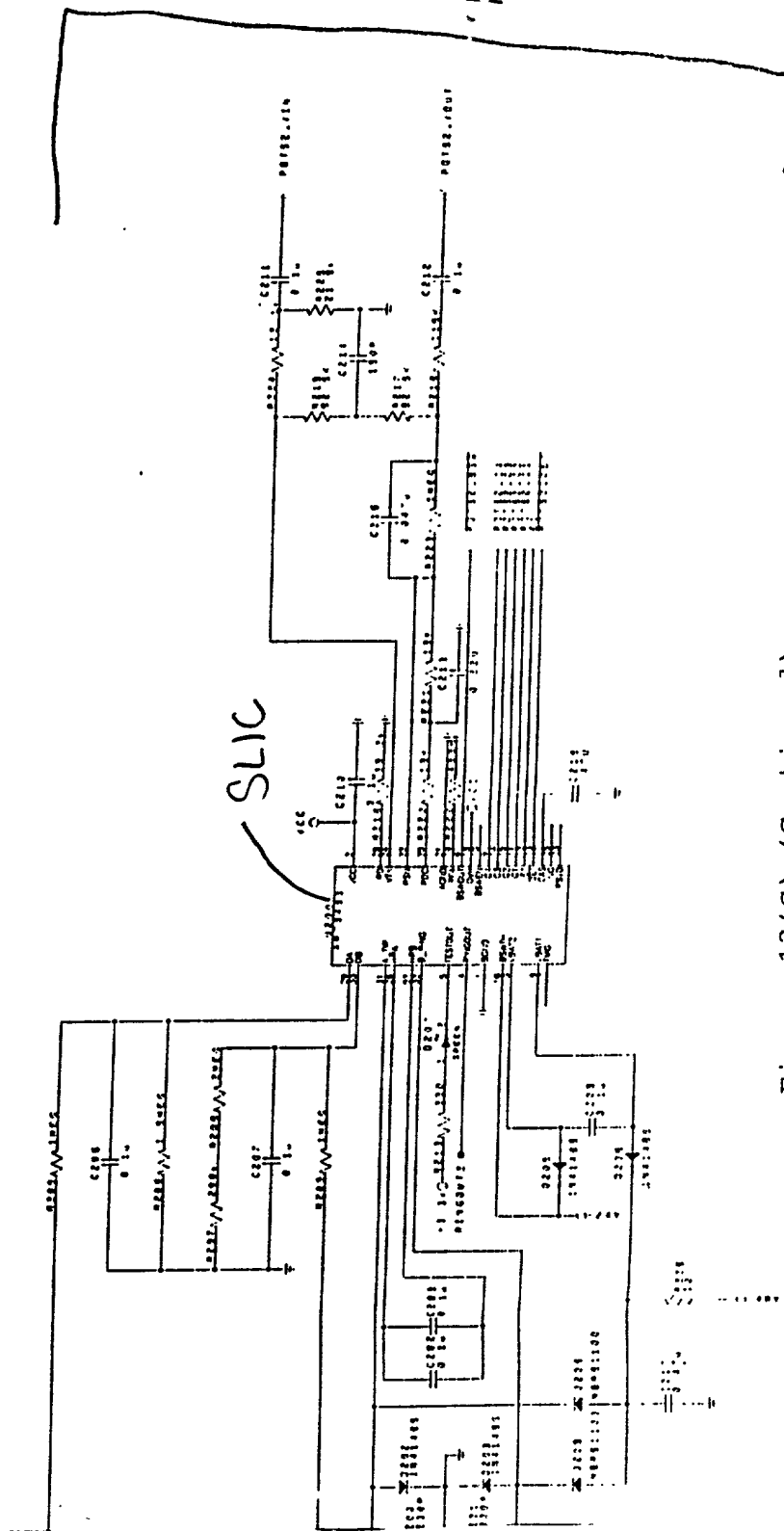


Figure 12(C) (Continued)

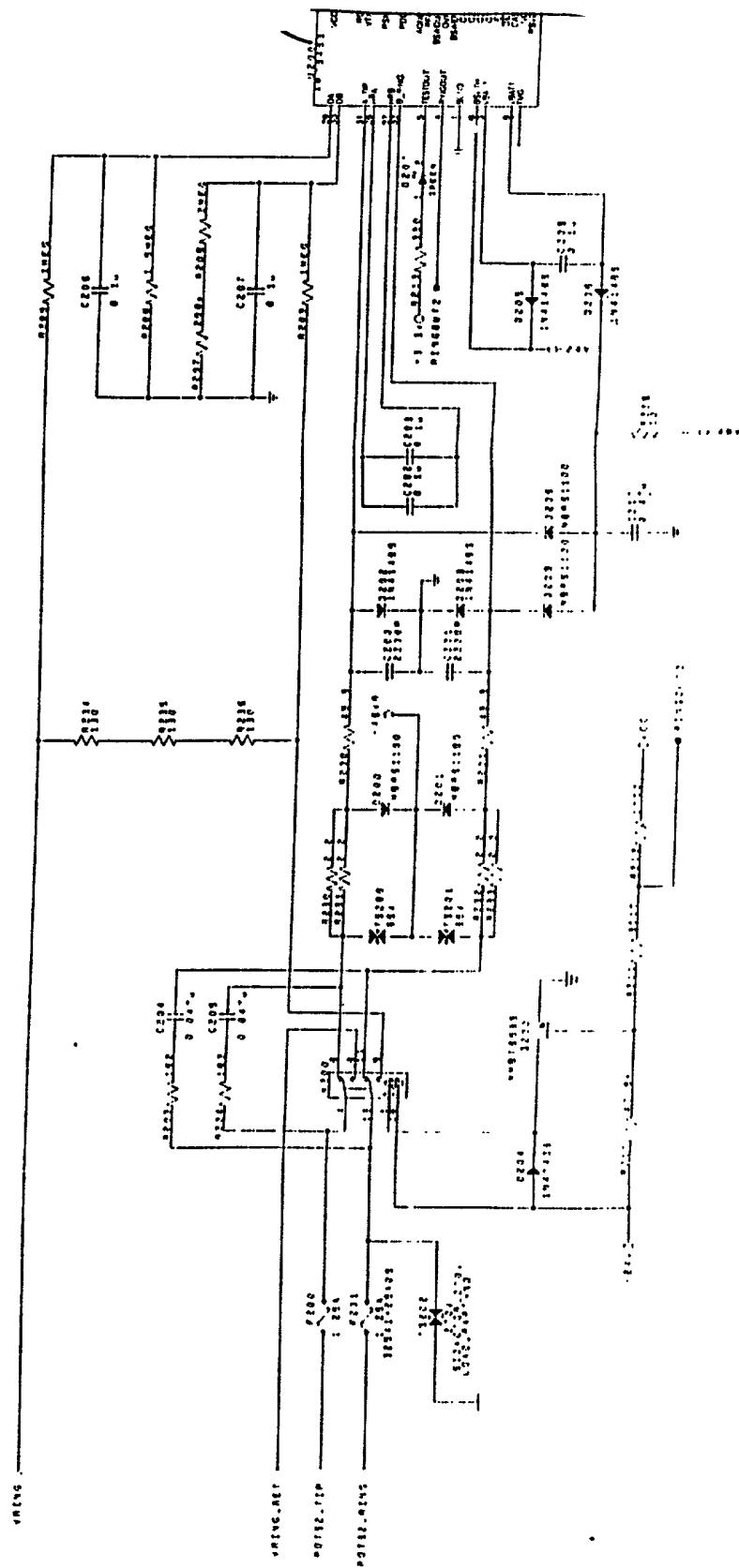
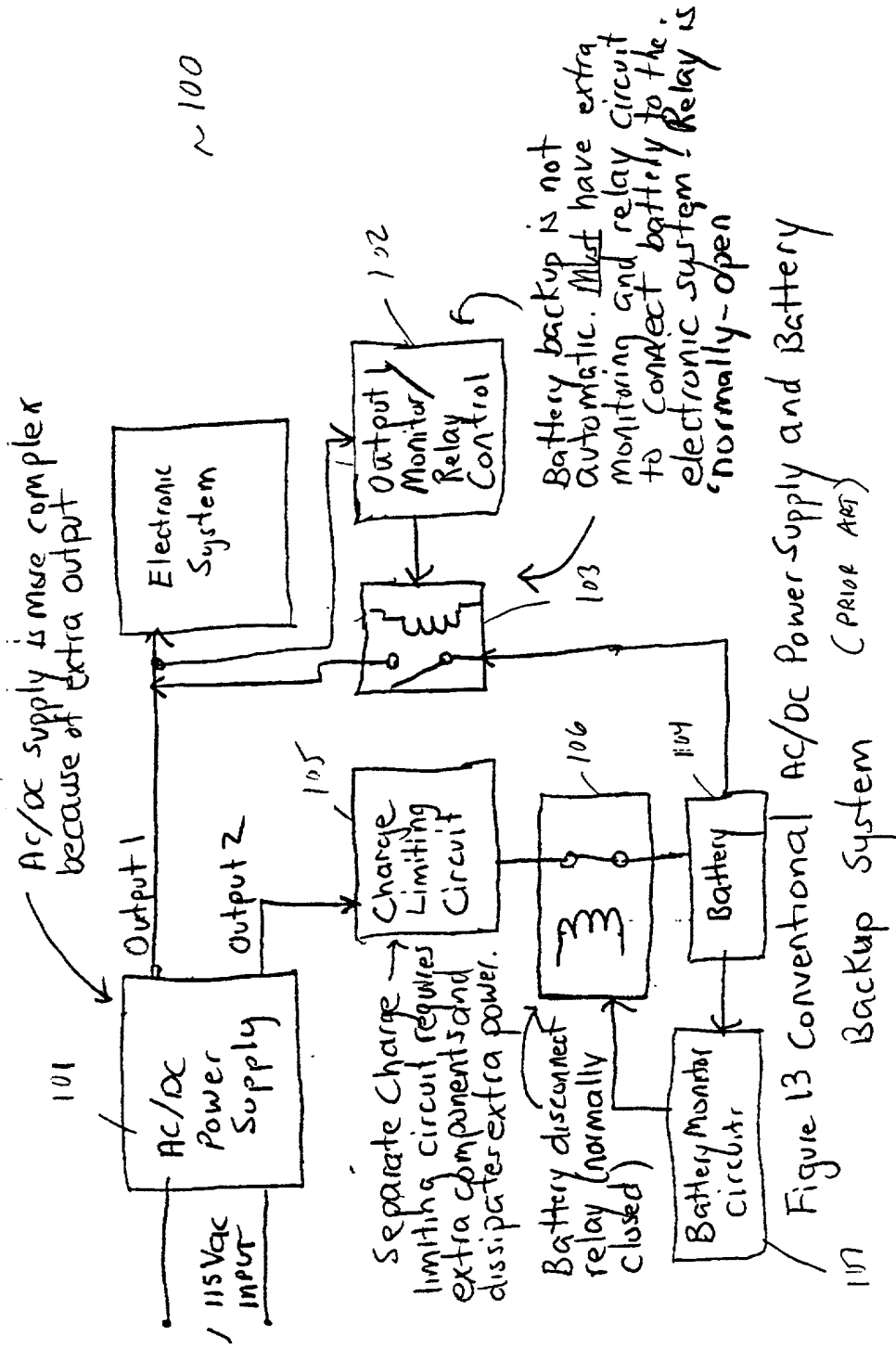


Figure 12(C) (Continued)



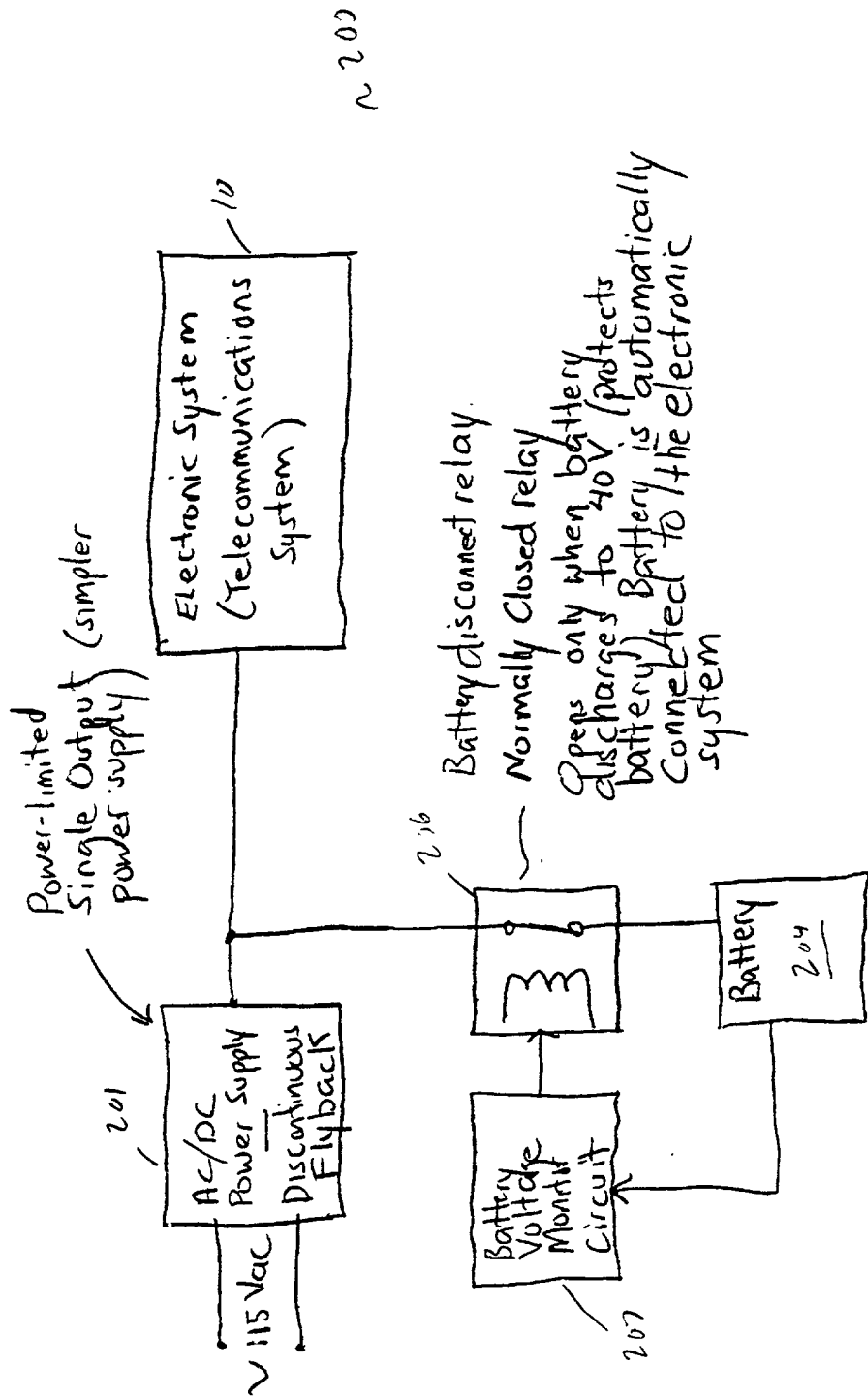


Figure 14 Block Diagram of New Power Supply/Battery Backup System

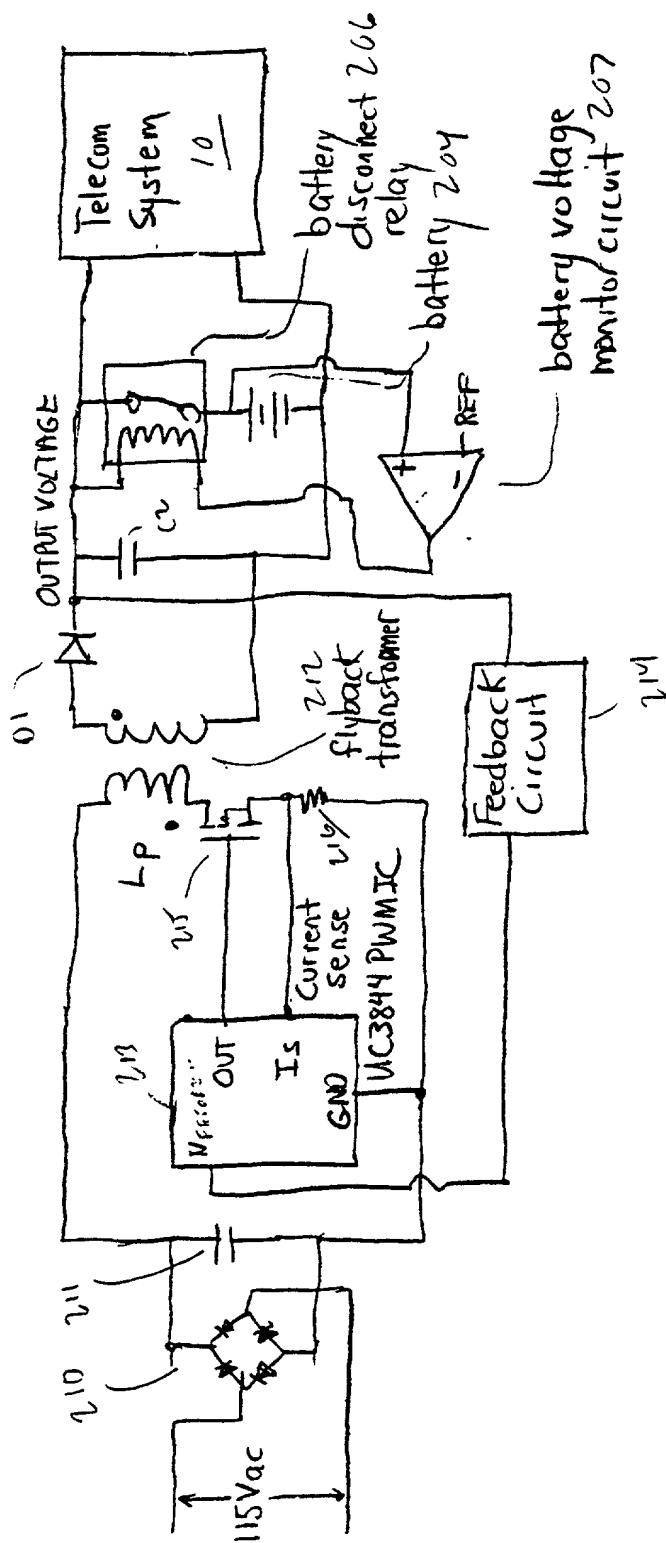


Figure 15 Simplified Schematic of New Power Supply/
Battery Backup System

Timing Diagram for SPI Receive Status Interface with all Access Modules (8 Bits)
Data coming from Access Module

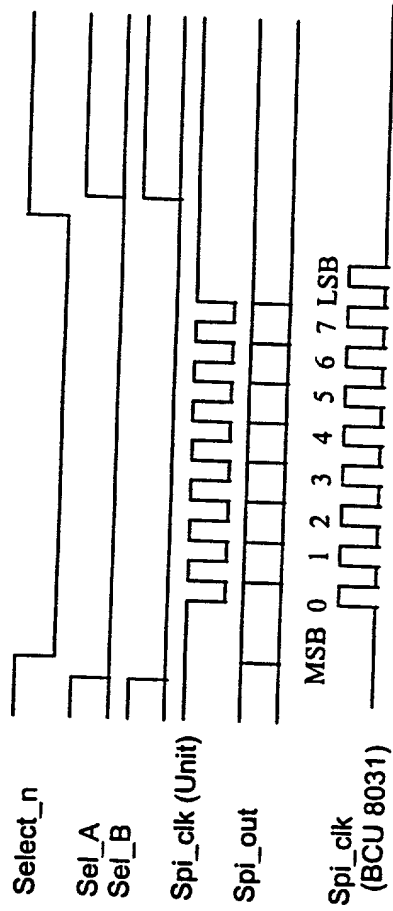


Figure 16

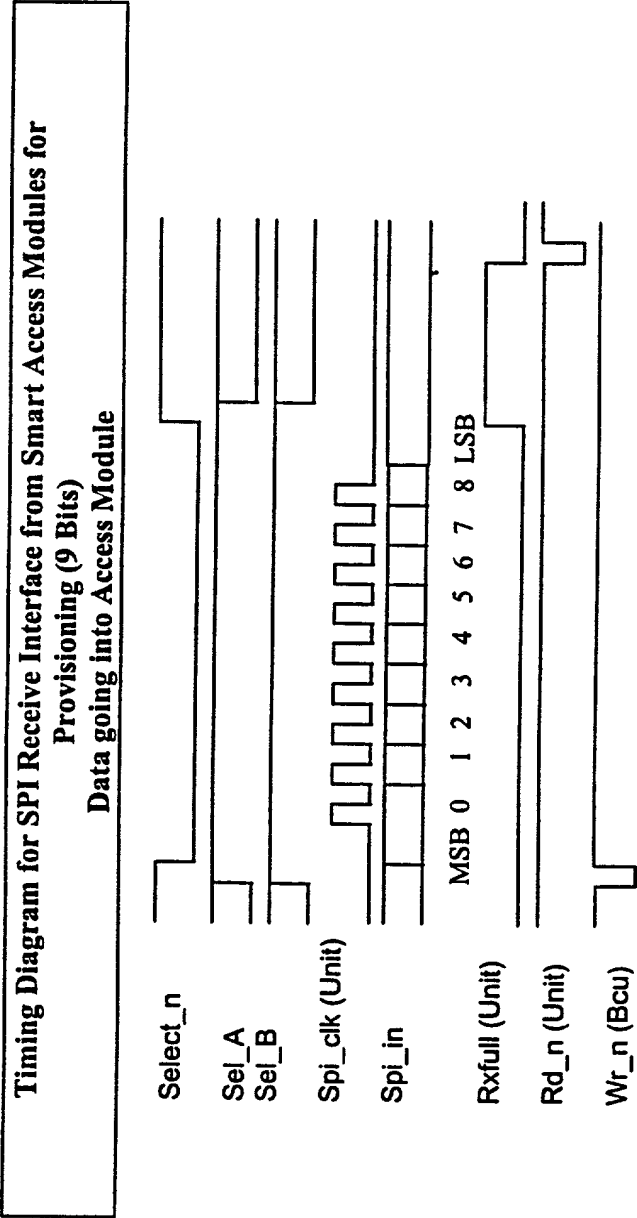


Figure 17

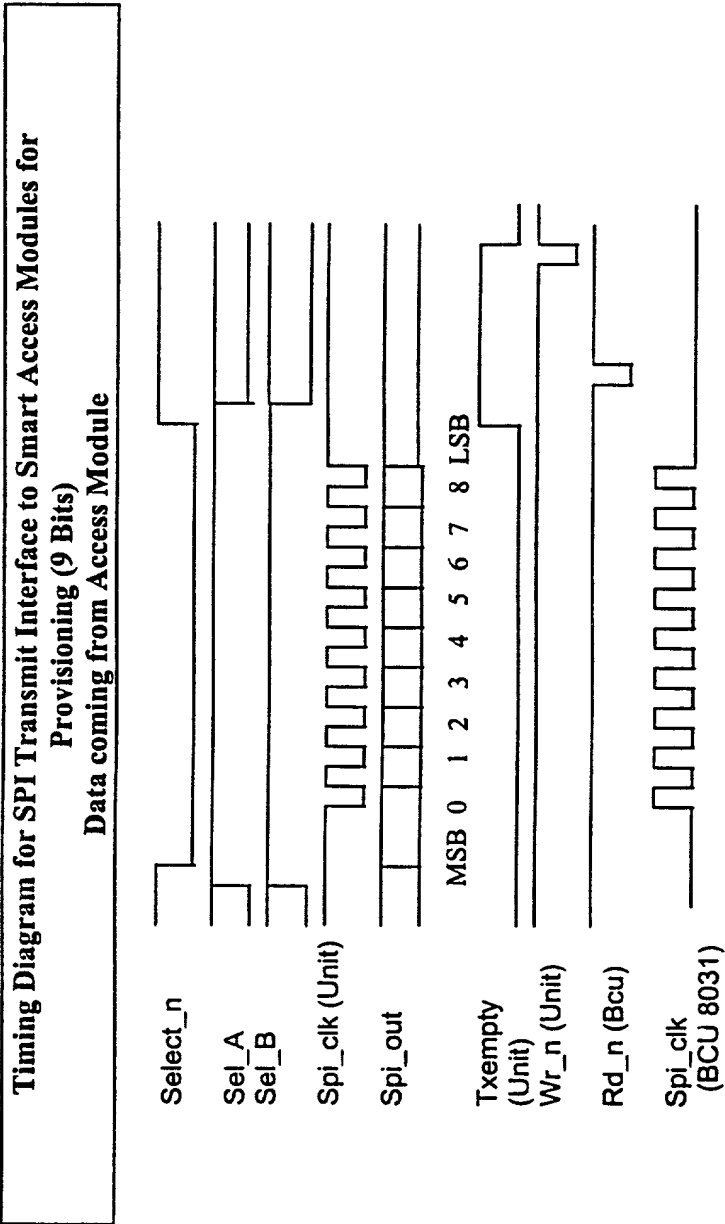


Figure 18

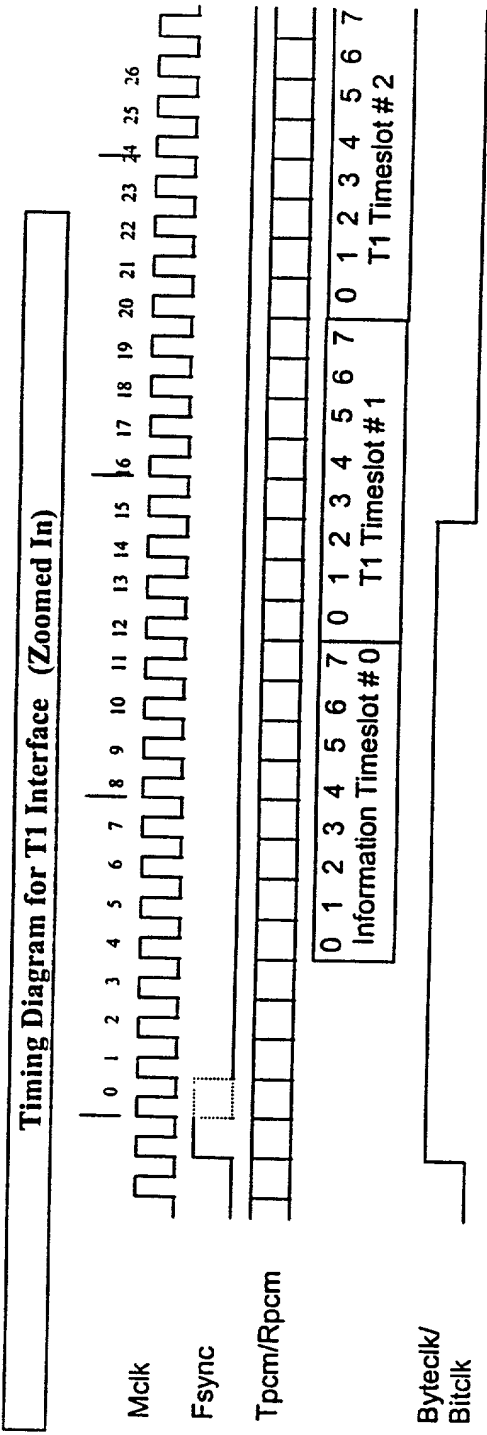


Figure 19

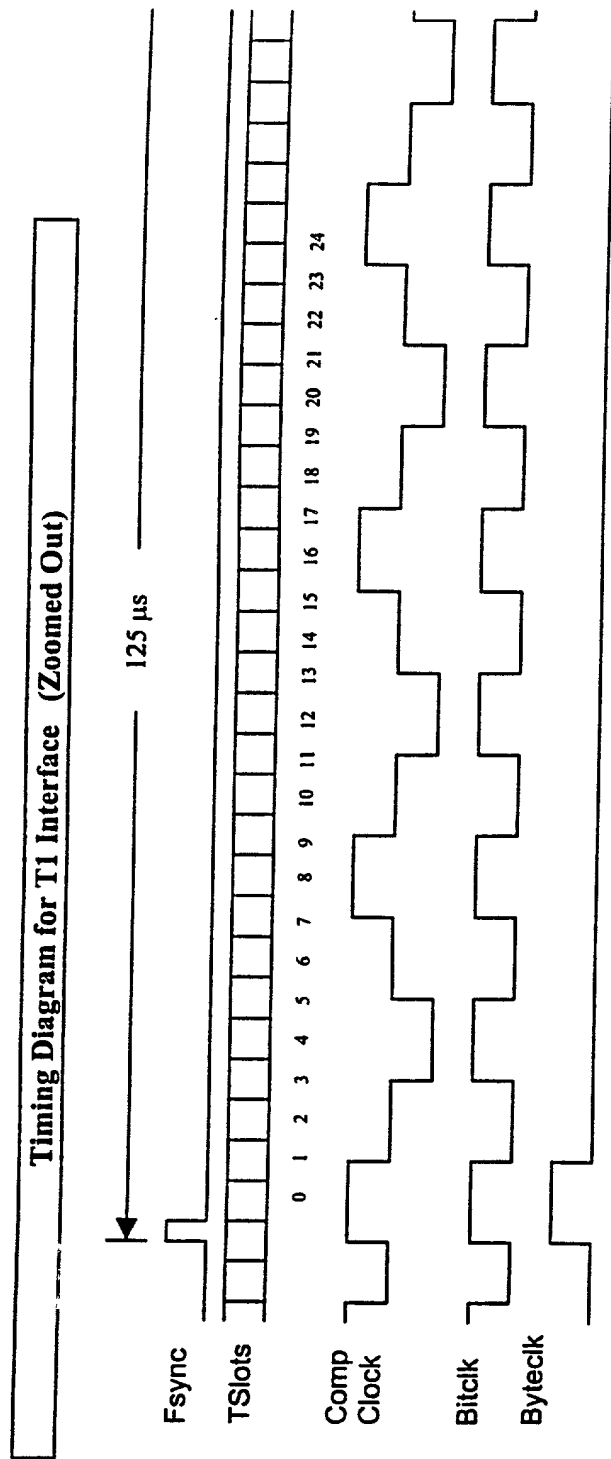


Figure 20